4.5 Working Group 10.5 (Design and Engineering of Electronic Systems)

The Working Group 10.5 "Design and Engineering of Electronic Systems" belongs to the IFIP Technical Committee 10, and was established in 1994. The WG 10.5 home-page is located at http://www.inf.ufrgs.br/ifip10-5. The main activity of the Working Group is in the frame of the International Conference on Very Large Scale Integration, IFIP-VLSI-SoC.

4.5.1 Members

The following persons became members of WG 10.5 in 2008:

Pr. Jose L. Ayala (Spain)

As a result, as of today the working group has 62 members.
Their geographic distribution is as follows:

North America - 16
Europe – 35
Asia / Pacific - 9
South America – 2

The following candidates will be invited for a presentation during ou next meeting in Brrazil:
- Pr. Tsui (Chi Ying) from Hong Kong, working on VLSI design. He offers to organize VLSI-SoC in 2011 in Hong Kong,
- Dr. Matthew Guthaus from University of California Santa Cruz, California. He offers to organize VLSI-SoC 2012 in California.

4.5.2 Officers

The officers of WG 10.5 are:
- Prof. Michel Robert (France), Working Group chair
- Prof. Ricardo Reis (Brazil) and Luis Miguel Silveira (Portugal), Vice-chairs
- Prof. Flavio Wagner (Brazil), Past chair

4.5.3 Meetings

The following business meetings were conducted by WG 10.5 during the last 12 months:

- Munich, Germany during DATE'08, March 10, 2008
- Rhodes Island, Greece , October 12th, 2008, before VLSI-SoC 2008
- Nice, France, April 20, 2009, during DATE conference
- San Francisco, July 31, 2009, during DAC conference

The following business meetings are planned for the coming year:
- Florianopolis, Brazil, on October 12 (2009), during VLSI-SoC 2009
- Dresde, Germany during the DATE conference in 2010.

4.5.4 Sponsored Events

The following event with main sponsorship by WG10.5 was held in the past 12 months:
The 16th Ifip International Conference on Very Large Scale Integration was held in Rhodes Island, Greece , from October 13th to 15th. The General Chair, was Pr. Dimitrios Soudris, member of the WG 10.5. Springer will publish a book with a selection of the best papers:
- Number of participants: 124
- Countries :   31 (Submissions from: Greece, France, Spain, Brazil, Canada, USA, Iran, Saudi Arabia, Turkey, Cyprus, Egypt, Tunis, Israel, Italy, Belgium, The Netherlands, Germany, Switzerland, Czech, Denmark, Sweden, China, Philippines, Taiwan, India, Bangladesh, Australia, Korea, Japan, Singapore, Norway)
- Number of submissions: 193 papers
- Total number of accepted papers: 98 papers
- Regular papers (6 pages) : 56 papers (acceptance rate : 29%)
- Poster papers : 42 (total acceptance rate : 51 %)
- Special sessions : 12 papers
- Total : (98 + 12) = 110 papers
- Number of reviewers : 165
- Number of reviews : asked for 193*3 (i.e., 3 reviewers) reviews, received 501 reviews : 2.6 reviews per paper
- 3 Keynote speakers :
"Interconnect-Based Design Challenges in High Performance Two- and Three-Dimensional Integrated Circuits and Systems" by Professor Eby G. Friedman,University of Rochester, USA,
"Design and Programming strategies for MPSoC" by Dr. Ahmed JERRAYA, CEA-LETI, MINATEC, France
"CLEAN: Leakage Aware Design for Next Generation's SOCs" by Dr. Ing. Roberto Zafalon, STMicroelectronics, Agrate Brianza (MILANO), Italy
- 1 Invited speaker, 4 Special Sessions, 56 oral presentations, 42 posters, 3 posters for IST European-funded projects, 10 poster presentations for Ph.D. Forum

4.5.6 Planned Events

The following main sponsored events are already planned:

IFIP-VLSI-SoC'09 – 17th International Conference on Very Large Scale Integration, will be held in Florianopolis, Brazil, from October 12 to 15, 2009

The next edition of EduTech will be held in Florianopolis, together with VLSI-SoC, in 2009 (October 15, 16)

IFIP-VLSI-SoC'10 – 18th International Conference on Very Large Scale Integration, will be held in Madrid, Spain from September 28 to 30, 2010

4.5.7 Special Interest Groups
The "green computing" topic will be discussed during our next meeting in Brazil.

Appendix

VLSI-SoC books
Springer is producing a book, each year, of the best contributions extracted from the IFIP-VLSI-SoC' conferences.

IFIP VLSI-SOC events:

- IFIP VLSI-SoC 2011: Hong kong, China
- IFIP VLSI-SoC 2010: Madrid, Spain
- IFIP VLSI-SoC 2009: Florianopolis, Brazil
- IFIP VLSI-SoC 2008: Rhodes Island, Greece
- IFIP VLSI-SoC 2007: Atlanta, Georgia, USA
- IFIP VLSI-SoC 2006: Nice, France
- IFIP VLSI-SoC 2005: Perth, Western Australia
- IFIP VLSI-SoC 2003: Darmstadt, Germany
- IFIP VLSI-SoC 2001: Montpellier, France
- IFIP VLSI 1999: Lisbon, Portugal
- IFIP VLSI 1997: Gramado (near Porto Alegre), Brazil
- IFIP VLSI 1995: Chiba, Japan
- IFIP VLSI 1993: Grenoble, France
- IFIP VLSI 1991: Edinburgh, Scotland
- IFIP VLSI 1989: Munich, Germany
- IFIP VLSI 1987: Vancouver, Canada
- IFIP VLSI 1985: Tokyo, Japan
- IFIP VLSI 1983: Trondheim, Norway
- IFIP VLSI 1981: Edinburgh, Scotland