

# Minutes of the 4th Meeting of IFIP WG 10.5

Paris (France), 12 March 1996

## Attendees

- Members present:

Ed Cerny, Alexander Seldmeier (representing Klaus Buchenrieder), Einar Aas, Przemyslaw Bakowski, Hans Eveking, K.-H. Diener, Ricardo Reis, Luc Claesen (chair), Carlos Delgado Kloos (secretary), Takayuki Yanagawa (vice-chair), Arno Kunzmann (representing Wolfgang Rosenstiel), Francois Anceau, Adam Pawlak, Arnim Bender (representing Werner Grass), Norbert Fristacki, Alexander L. Stempkovsky, Franz Rammig, Giovanni De Micheli, Pierre Paulin, Alain Greiner, Wolfgang Nebel, Gabrielle Saucier, Peter Schwarz, Manfred Glesner, Jean Mermet (also representing Gerry Musgrave).

- Apologies received from:

Dominique Borrione, Klaus Buchenrieder, Francisco Corella, Nikil D. Dutt, Masahiro Fujita, Dan Gale, Steven D. Johnson, George J. Milne, Richard A. Newton, Jørgen Staunstrup, Pasupathi A. Subrahmanyam, Carlos López Barrio, Gerry Musgrave.

## 1. Approval of previous minutes

The minutes were approved.

## 2. Membership

M. Newman resigns. There are no new members.

## 3. Next meetings

The next meetings were planned as follows:

- An informal meeting will be held in Geneva, Switzerland on the occasion of EuroDAC'96 with EuroVHDL'96.
- The following formal meeting will be held in Santa Clara, Ca., USA November 96 on the occasion of ICCAD'96.

## 4. Event request forms

All members organizing events related to IFIP WG 10.5 (either under the sponsorship scheme or in cooperation) are urged to fill out the event request forms. They are included once again attached to these minutes.

## 5. Special Interest Groups

**SIG-VHDL** It was informed that the 96 VHDL Forum will be held in May 1996 in Dresden, Germany. The forms have been signed. The Fall Forum will be held in conjunction with EuroVHDL'96, organized by the French Speaking Users Group. 47 papers have been received for EuroVHDL'96. The 97 VHDL Forum will be held in April 1997 in Toledo. PC Chair is Eugenio Villar. It was agreed the IFIP WG 10.5 delegates the control of the *Workshop on Libraries, Component Modelling and Quality Assurance* to SIG-VHDL, who will sponsor it.

**SIG-CODES** It was informed that a Working Conference on HW/SW Co-Design has been announced to be held in Zakopane, Poland, 29–31 August 1996. Also the COBRA Workshop will take place 29–30 April in Grassau, Germany. A WWW-page has been set up. Its URL is: <http://www.fzi.de/esm/IFIP10.5.SIG-CODES/sig-c>

The SIGs were asked to send an annual report. The one of SIG-VHDL is attached.

## 6. Budget

G. Musgrave would like to have a cushion in order to have more flexibility. Luc Claesen asks to support this motion. It was approved unanimously. It was also proposed to have working money to support small events (max. 2000 US\$ per workshop). This was also approved unanimously. G. Musgrave will ask for 10000 US\$ working money.

## 7. Past events

1. ASP-DAC'95 (Asian Pacific Design Automation Conference) [Prof. T. Ohtsuki, Dr. T. Yanagawa]  
The audit has been passed in January 1996. There were some problems due to fluctuations in the exchange rates, which had to be solved.
2. APCHDL'96 (Asian Pacific Conference on Hardware Description Languages)  
Has been very successful. A final statement is attached. The next conference in this series will be held in Taiwan.
3. Logic and Architecture Synthesis  
There has been a good connection to Japan. Chapman & Hall has published the previous proceedings (Dec. 95), but will not publish the next one (Dec. 96).
4. Reed Muller expansion  
A report is attached. The next event in the series is planned for September 1997 in Oxford.
5. CHARME'95  
It was held in Frankfurt in October 95 with 40–45 participants. The proceedings will appear in the LNCS series of Springer.
6. ISSS'95  
There have been around 100 participants, and the general impression has been very good.

## 8. Planned events

1. CHDL'97  
It is planned to hold three other events in conjunction with CHDL'97: the *Spring '97 Working Conference of the VHDL Forum for CAD in Europe*, the *Workshop on Libraries, Component Modelling and Quality Assurance*, and the *Esprit NADA workshop* (about *New Hardware Design Methods*).
2. VLSI'97
3. EuroDAC'95 (European Design Automation Conference)
4. EuroVHDL'95

5. Workshop on Logic and Architecture Synthesis  
INPG, Grenoble, 18–19 December 1995  
General chair: Gabrièle Saucier, Program Chair and Organization: Anne Mignotte  
The *in cooperation status* was approved.
6. 2nd International Symposium on Advanced research in Asynchronous Circuits and Systems (Async96)  
Aizu-Wakamatsu, Fukushima, Japan, 18–21 March 1996  
General Chair: Toshiyasu Kunii, Conference Co-chairs: Takashi Nanya, Alex Kondratyev  
The *in cooperation status* was already approved by email vote.
7. 3rd International Conference on Electronics, Circuits, and Systems (ICECS'96)  
Rodas, Greece, 13–16 October 1996  
General chair: Thanos Stouraitis, Technical Program Chair: T. Deliyannis  
The *in cooperation status* was **not** approved.

## 11. New activities

Luc Claesen asked for ideas for new topics and activities. Alexander Sedlmeier proposed *Analog Prototyping*. Further ideas are encouraged.

## 12. Any other Business

The issue about publishers was discussed. Franz Rammig was asked to prepare a short letter of opinion to be sent to the Publishing Committee of IFIP.

It was proposed to talk about VLSI'99 at the next meeting.