

Minutes of the 2nd meeting of IFIP WG 10.5

Paris, 9 March 1995

Attendees

- Members present: F. Anceau, P. Bakowski, K. Buchenrieder, L. Claesen (chair), C. Delgado Kloos (secretary), H. Eveking, A. Kunzmann (representing W. Rosenstiel), J. Mermet, G. de Micheli, W. Nebel, A. Pawlak, F. Rammig, P. Schwarz, J. Staunstrup, A. Stempkovsky, T. Yanagawa
- Invited persons: N. Fristacki (TC10 member), R. Reis
- Apologies received from: K.H. Diener, W. Rosenstiel (represented by A. Kunzmann), G. Milne, Sabine März-Rössel, T. Ohtsuki, A. Halaas, J. Darringer, P. Menchini, N. Dutt, F. Brglez, S. Furber, D. Rees, W. Damm, D. Gale, R. Camposano, S. Johnson, E. Cerny, H. Ofek, R. Waxman, G. Musgrave, R. Hartenstein, F. Wagner, M. Sami, N. Kawato, P. Prinetto

1. Approval of previous minutes

The minutes were approved as a true record of the last meeting. L. Claesen thanked the organizers of ED&TC for renting the room to hold the meeting.

2. Updates of address information of members

Done.

3. Next meetings

The next meetings were planned as follows:

- The next meeting was decided to be held in Japan on the occasion of CHDL'95 and VLSI'95 in August/September 1995. The exact dates are to be decided by email.

- An informal meeting will be held after that in Brighton, UK on the occasion of EuroDAC with EuroVHDL '95.
- The following meeting will be held in Paris, France on the occasion of EDT&C'96, on Tuesday 12 March 1996, 18:30h.
- The meeting after that will be in Geneva, Switzerland, on the occasion of EuroDAC with EuroVHDL '96, at the end of September 1996.

4. New members

The following new members were approved:

- Prof. Kurt Antreich
- Prof. Wolfgang Nebel
- Prof. Ricardo Reis

Prof. Antreich's membership was approved by a fax vote. It was commented that his c.v. was too short. In future, c.v.s should give more details about the candidate.

L. Claesen proposed Prof. Norbert Fristacki as a new member. He will provide a c.v. to be voted at the next meeting [the c.v. has been distributed by email].

C. Delgado Kloos proposed Prof. Carlos López Barrio as a new member. His c.v. was distributed at the meeting. The voting will take place at the following meeting.q

5. Aims and scope of IFIP WG 10.5 approval

The aims and scope were read by Luc Claesen. They are going to be distributed by email for discussion and proposals of text updates. After 2 or 3 weeks there will a vote if there are no conflicting updates. Otherwise the decision will be taken at the following meeting.

6. Special Interest Groups

The statutes and bylaws had been approved for both SIG-VHDL and SIG-CODES by a fax vote. In both cases there were 25 votes in favour, 1 against and 1 abstention.

6.1 SIG-VHDL

W. Nebel reported about SIG-VHDL as its new chair. There are now 32 members. Donatella Sciuto is secretary, Alain Vachoux, treasurer and Eugenio Villar, representative of the national user groups.

The coming VHDL-Forums will be held in Nantes (Spring 95), Brighton (Autumn 95, in conjunction with EuroDAC/EuroVHDL '95) and possibly in Dresden (Spring 96, organized by P. Schwarz at the Fraunhofer Gesellschaft).

J. Mermet asked about the surplus of the Forum. ECSI has been giving support in the past and all the surplus has been transferred to IFIP. Mermet complained about the treasurer of WG 10.5.

It was pointed out that there should be a member of IFIP WG 10.5 representing the WG in the SIG-VHDL. P. Bakowski was proposed as a candidate and approved unanimously.

6.2 SIG-Codes

K. Buchenrieder reported about SIG-CODES as chair. The vice-chair has to look for a treasurer, otherwise he becomes the treasurer himself. The statutes were approved.

There will be a focus meeting on the occasion of EuroDAC/EuroVHDL '95 in Brighton. Edna Barros will hold a local meeting in January 96. A CODES/CASHE workshop (ACM/IFIP/IEEE) will be held 1996 in Pittsburgh.

The target is to aim towards 50 members, of which at least 50% should be paying members. In connection with the budget, it was discussed whether 600.– DM should be taken to pay the registration of one participant from an Eastern country who had a paper the CODES and couldn't pay the registration fee. After some discussion, it was approved that K. Buchenrieder could proceed in the way he thought most suitable.

K. Buchenrieder asked to have an own bank account for the handling of membership fees, because the treasurer would be taxed personally in the US. This is however not possible due to IFIP rules. He was referred to G. Musgrave.

It was also pointed out, that there should be a member of IFIP WG 10.5 representing the WG in the SIG-CODES. Since there are no members yet in SIG-CODES, this question remains unanswered.

7. Budget

Since the treasurer G. Musgrave was not present, only specific points were approved.

It was agreed to reserve a part of the budget for the mailing costs that the secretary is covering. The budget of 2 000 SF for this purpose was approved.

It was talked about the possibility of having grants for events in the budget of WG10.5. It was decided that the organizer of each event could put an item

in the budget of the event. This should not exceed the amount asked as a loan from IFIP.

8. Working group communication via email

It was approved to to continue communication via email. Votes can be organized via email, but since voting on persons can be critical, the chair, depending of the sensitivity, has the prerogative to organize voting by email, fax or in meetings.

9. Planned events

1. CHDL'95 (Computer Hardware Description Languages and Their Applications) [Prof. S. Johnson, Prof. T. Ohtsuki]
Will be one track of the joint ASPDAC/CHDL/VLSI'95 conference. There were 40+ submissions. The PC meeting will be held on Sunday 23 April 1995 in Austin, Texas.
2. VLSI'95 [Prof. W. Rosenstiel, Prof. T. Ohtsuki]
Will be one track of the joint ASPDAC/CHDL/VLSI'95 conference. There were 40+ submissions.
3. ASP-DAC'95 (Asian Pacific Design Automation Conference) [Prof. T. Ohtsuki, Dr. T. Yanagawa]
Will be two tracks of the joint ASPDAC/CHDL/VLSI'95 conference. There were 80+ submissions. The joint conference will be sponsored by ACM, IEEE and IFIP. The proceedings will be printed by IEEE Press. 700 copies will be printed. The recommendation of the WG is that IEEE will give 5\$ royalties to IFIP.
4. CHARME'95 (Correct Hardware Design Methodologies Workshop) [Prof. H. Eweking]
The Call for Papers is out.
5. EuroDAC'95 (European Design Automation Conference) [Prof. G. Musgrave, Prof. W. Rosenstiel]
Around 140 papers have been submitted. Altogether for EuroDAC+EuroVHDL there have been around 200 submissions (400 the previous year).
6. EuroVHDL'95 [Prof. G. Musgrave, Prof. W. Nebel]
62 papers were submitted (58 the year before).
7. CAV'95 (Computer-Aided Verification) [Prof. E. Clarke]
To be held in Liege, Belgium, starting on July, 3, 1995.
8. ISSS'95 (International Symposium on System Synthesis) [Dr. P. Paulin]

9. VHDL-Forum Spring 95 [Prof. P. Bakowski]
The provisional Program has been sent out.
10. Working Conference on VLSI for Image Processing and Vision Systems [Prof. P. Ivey]
11. Workshop on Synthesis onto FPGA and Technology Migration [Prof. G. Saucier]
12. Workshop on Front End Design Tools [Dr. D. Gale]
13. Workshop on Libraries, Component Modeling and Quality Assurance [Prof. D. Borrione, Dr. A. Pawlak]
Will be held in conjunction with the VHDL-Forum Spring'95.

10. Future 10.5 conferences

1. CHDL'97 & VLSI'97
C. Delgado Kloos made a presentation about his proposal to hold CHDL'97 in Toledo (Spain). R. Reis made a presentation for holding VLSI'97 in Canela (Brazil) and A. Stempkovsky for holding it in Russia.
It was decided to hold CHDL'97 in Spain, with C. Delgado Kloos as general chair and VLSI'97 in Brazil, with R. Reis as general chair. Russia could be a possible candidate for VLSI'99.
2. ED&TC'96 (European Design & Test Conference)
In cooperation status approved.
3. TPCD'96 (Theorem Provers in Circuit Design)
Will be held in Palo Alto organized by Srivas (SRI). In cooperation status approved.
4. ISSS'95 (International Symposium on System Synthesis) [Dr. R. Camposano]
5. ACID'95
In cooperation status **not** approved.
6. CODES'96 [K. Buchenrieder]
Will be held in March 96 in Pittsburgh. The ACM/IEEE forms have already been filled in. The IFIP forms are still to be filled in. For decisions ACM has two votes (SIGDA and SIGSOFT), IEEE has two votes (Computer Society and Circuits and Systems), and IFIP has two votes.

7. APCHDL'96 (Asian Pacific Design Automation Conference) [Prof. N. Dutt]
Sponsorship requested by N. Dutt. This was approved, putting an action upon N. Dutt, to include as many WG 10.5 members as possible in the Program Committee in order to have a better control of the conference.
8. East-West Cooperation Workshop in Slovakia [Dr. A. Pawlak]
In cooperation status approved.
9. Summer-School on Hardware-Software Co-Design [Prof. J. Staunstrup]
To take place in August 1995 in Lyngby, 1 week of duration. In cooperation status approved. It has to mention IFIP WG10.5 SIG-CODES.
10. SIG-CODES focus meeting
Will be held in Brighton at the Metropole Hotel on Friday 26 Sept. 1995. The general chair will be M. Edwards, the organizing chair J. Forrest. The program chair is still to be decided. Sponsorship approved.

11. Past events

1. CODES/CASHE'94 [K. Buchenrieder]
There were 80 participants from 13 countries. A surplus of 20 000.—DM was made.
2. EuroDAC'94/EuroVHDL'94 [Prof. G. Musgrave, Prof. W. Rosenstiel, Prof. W. Nebel]
There were 1143 registered participants at the Conference. It was approved that T. Yanagawa continues to act as WG 10.5 representative at EuroDAC.
3. Symposium on Advanced Research in Asynchronous Circuits and Systems [G. Gopalakrishnan]
Took place in Salt Lake City, 3–5 November 94
4. EDAF'94 (Electronic Design Automation Frameworks) [Prof. F. Wagner]
There was a loss of US\$ 1000. 25 papers, 2 invited speakers, 43 attendees (only 2 from the US). The proceedings will appear at Chapman&Hall. F. Rammig will send a report.

12. Any other business

The meeting ended at 18:00h.