



IFIP Working Group 10.5 Minute Meeting

April 18, 2023 11am-13am

Antwerp (BE)

Notes taken by Graziano Pravadelli

Attendees in presence: Katell Morin-Allory, Achim Rettberg, Florenc Demrozi, Victor Grimblatt, Matteo Sonza Reorda, Said Hamdioui, Ian O'Connor, Graziano Pravadelli, Nicola Bombieri, Luig Miguel Silveira, Pierre-Emmanuel Gaillardon, Matthew Guthaus, Eugenio Villar, Chi Ying Tsui, Jurgen Becker.

Attendees in remote mode: Ibrahim Elfadel, Andrea Calimera, Salvador Mir, Tiziana Margaria.

Apologies: Dimitrious Soudris, Luc Claesen, Sara Vinco, Srinivas Katkoori, Carlos Cardena, Ioana Vataljelu, Dominique Borrione, Fatih Ugurdah, Ricardo Reis, Nickil Dutt, Manfred Gleasner, Srinivas Katkoori.

Agenda

1. Presentation of new members
2. Communications
3. Mapping of WG competences
4. Status of VLSI-SOC 2023 and 2024
5. Any other business

1. Presentation of new members

We have 1 new candidature by Prof. Florenc Demrozi from University of Stavanger, Norway. The candidate presents his research activities which focus in particular on Telemicine, IoT and IoMT, Human Activity Recognition and Virtual Coaching.

After the presentations, we unanimously approved the admission of the candidate to the working group.

2. Communication

- Prof. Achim Rettberg, TC 10 chair, thanks all WG members for their activities. He kindly asks to apply for PhD scholarships in advance for the VLSI-SOC conference.
- The chair reports that the WG 10.5, up to now, is going to sponsor two conferences for 2023: VLSI-SOC that will be in Dubai, UAE on Oct 16-18, and FDL that will be in Turin, Italy, on Sept 13-15.

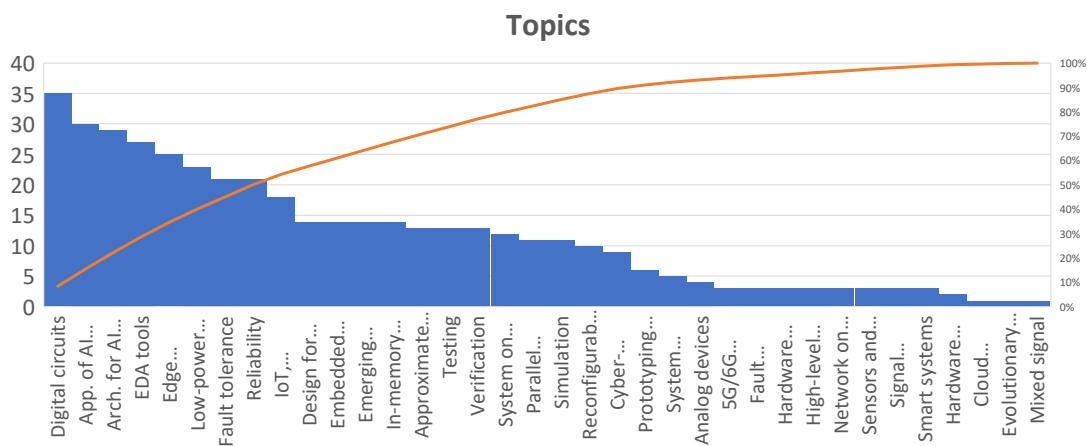
- The chair asks prof. Margaria to present the R@ISE initiative at U. Limerik, a catalyst for the development of a strong Low Code/No Code research and adoption capability, launched on April 4 2023. Prof. Margaria describes the initiative with plenty of details. WG members interested to cooperate are invited to contact Prof. Margaria. Further information are available at: <https://immersive-se.ie/raise>.
- The chair asks prof. Bombieri to present the possibility of organizing a WG 10.5 Summer School in the context of the Lipari Summer School (<https://liparischool.it>), in Italy on July 2024. Prof. Bombieri describes the history of the summer school, which is well consolidated since a while. Every year there is the possibility of proposing a lectures on selected topics. The WG members are invited to suggest the theme and possible speakers.
- The chair remembers the deadlines for the nomination of candidates for the IFIP awards
 - IFIP fellow: April 30
 - IFIP silver core award: July 15
 - IFIP service award: July 15
 - IFIP WG10.5 Meritorious Service Award: September 30
- The chair remembers that his 3-year terms will finish next October. The election of the new chair will be done during the WG meeting co-located with VLSI-SOC 2023. Candidatures are welcome.

3. Mapping of WG competences

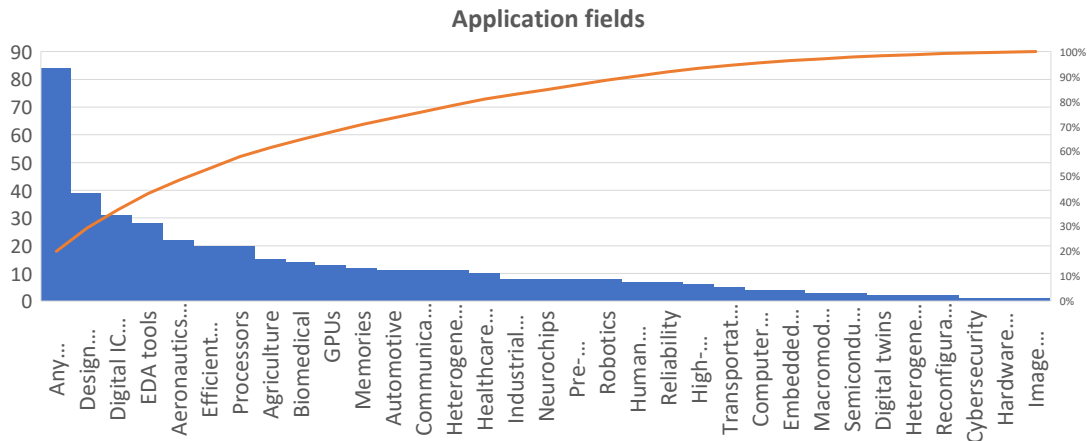
The status of the mapping is reported in the following pictures.



Preliminary results (17 answers)



Preliminary results (17 answers)



17

Next steps are:

- To describe the topic by including keywords (core technologies)
- To map members on the topics
- To create the core technology cloud

4. Status of VLSI-SOC 2023

Ibrahim Elfadel (Progam chair) presented the status of VLSI-SOC 23.

- Dates: October 16-18, 2023
- Location: American University of Sharjah, UAE
- Theme of the conference: VLSI SoC Innovations for Trustworthy AI
- Current sponsors: IFIP, IEEE CEDA, IEEE CAS, American University of Sharjah, Khalifa University, Rodhe&Swarz, Cadence, University of Sharjah
- Organizing committee is completed.
- Conference tracks are:
 - Analog, mixed-signal, RF, and sensors.
 - Design for testability, reliability, and fault tolerance.
 - Digital circuits and systems: SoC, NoC, reconfigurable, and low-power architectures.
 - Special applications: biomedical, hardware security, and space.
 - Design for AI acceleration and SoC design for machine learning.
 - IoT and CPS for the digital transformation.
 - Signal processing and communications for 6G.
 - Emerging computing paradigms: in-memory, quantum, and cryogenic.
 - Modelling, simulation, verification, and prototyping.
 - EDA tools and methodologies for IC design.
 - Power Electronics Devices and Circuits.
- CFPs for conference, PhD Forum and Student Forum are ready.
- Web site is ready at <https://www.vlsisoc2023.com>
- Easychair has been setup

- Four keynotes are foreseen
 - Dr. Chris Clifton, CTO, Sony Semiconductors Europe, UK
 - Dr. Zeyad Hanna, Oxford University, Cadence Chief Scientist, USA
 - Prof. Stavros Iezikel, University of Cyprus
 - Mr. Ahmed Shafqat, Rohde & Schwarz, Germany
- Application submitted to IEEE and IFIP.
- Everything is completed for IEEE proceedings.
- Contact with Springer for the book activated.
- Hotel discounts are in negotiation.
- Transportation from Dubai to AUS campus will be negotiated with the hotels offering discounts
- Registration fees are as follows:

EARLY REGISTRATION	
<i>Last Date: 3rd September 2023</i>	
Category	Registration Fees USD \$
IFIP/IEEE/ACM Members Discounted Registration	600
Non-Members	720
Student registration (Regular paper with IEEE indexing)	300
PhD Student Forum	150
Student Forum	150

LATE/ON-SITE REGISTRATION	
<i>Start Date: 4th September 2023</i>	
Category	Registration Fees USD \$
IFIP/IEEE/ACM Members Discounted Registration	700
Non-Members	840
Students registration (Regular paper with IEEE indexing)	350
PhD Student Forum	150
Student Forum	150
IFIP WG10.5 Conference Steering Committee Members	0

5. Status of VLSI-SOC 2024

Said Hamdioui summarizes key points for the organization of VLSI-SOC 2024 in Tangier.

- The structure of the overall program proposed during the past meeting in Patras was confirmed, including 2 tutorials on Sunday before the conference, 3 keynotes, 2 panels, regular and special sessions, poster session, and PhD/student forums.
- He proposed to identify and involve women for some of the key roles and to create a TPC with a mix of junior and senior members to guarantee the evolution of the conference in the future years.
- Everything is going well.

8. Any other business

None

Next meeting: at VLSI-SOC, October 2023.